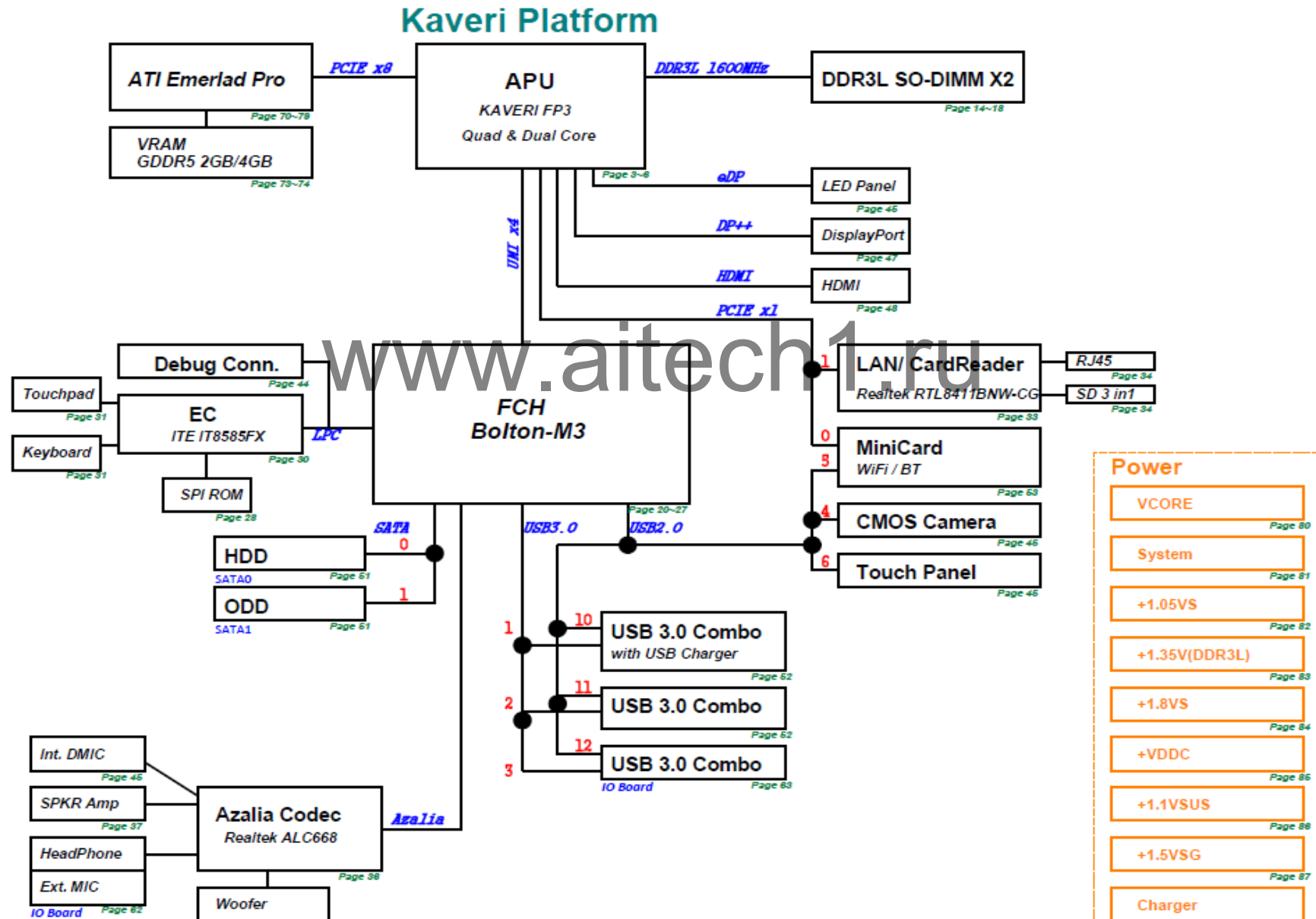
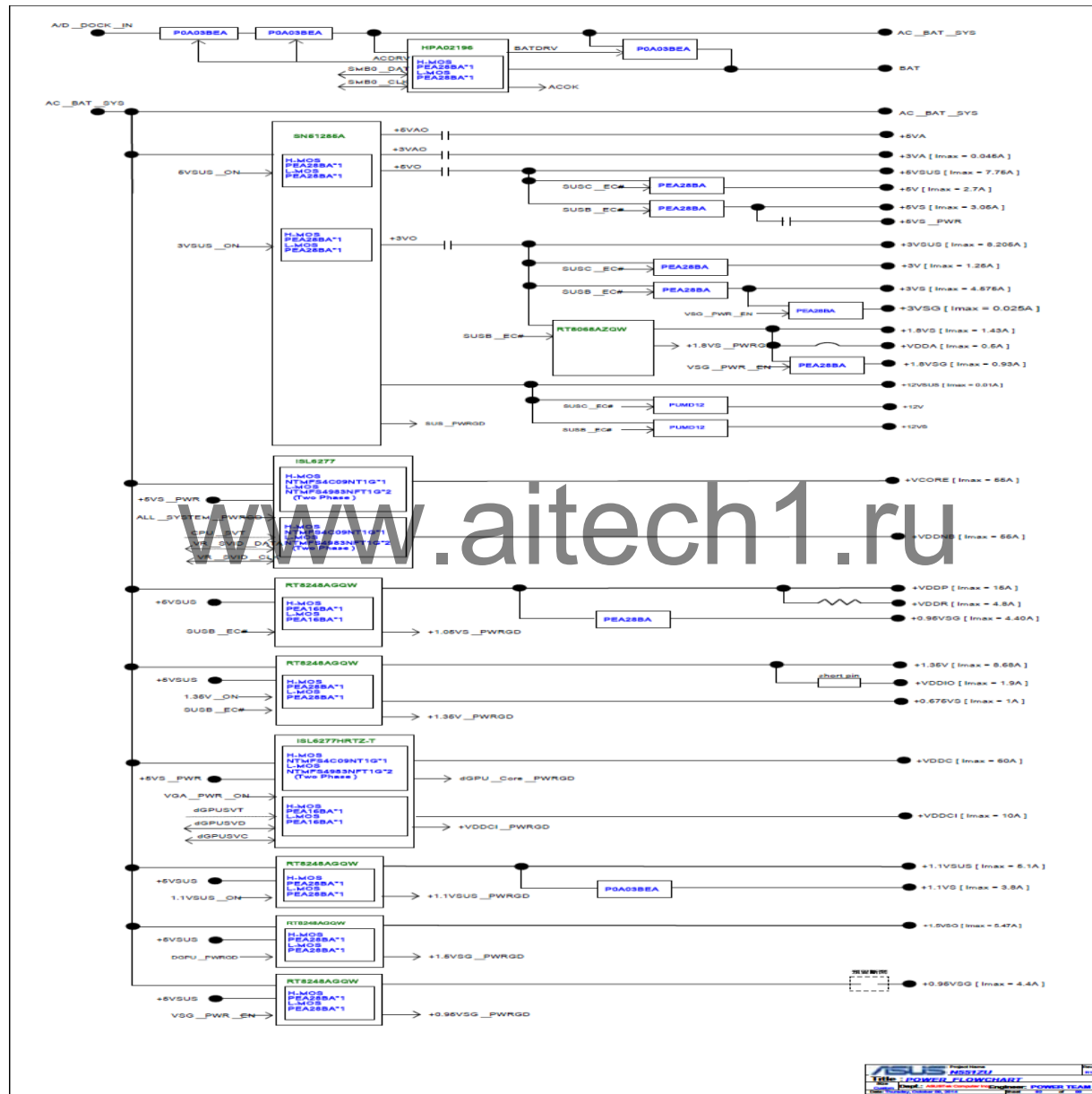


N551 Block Diagram

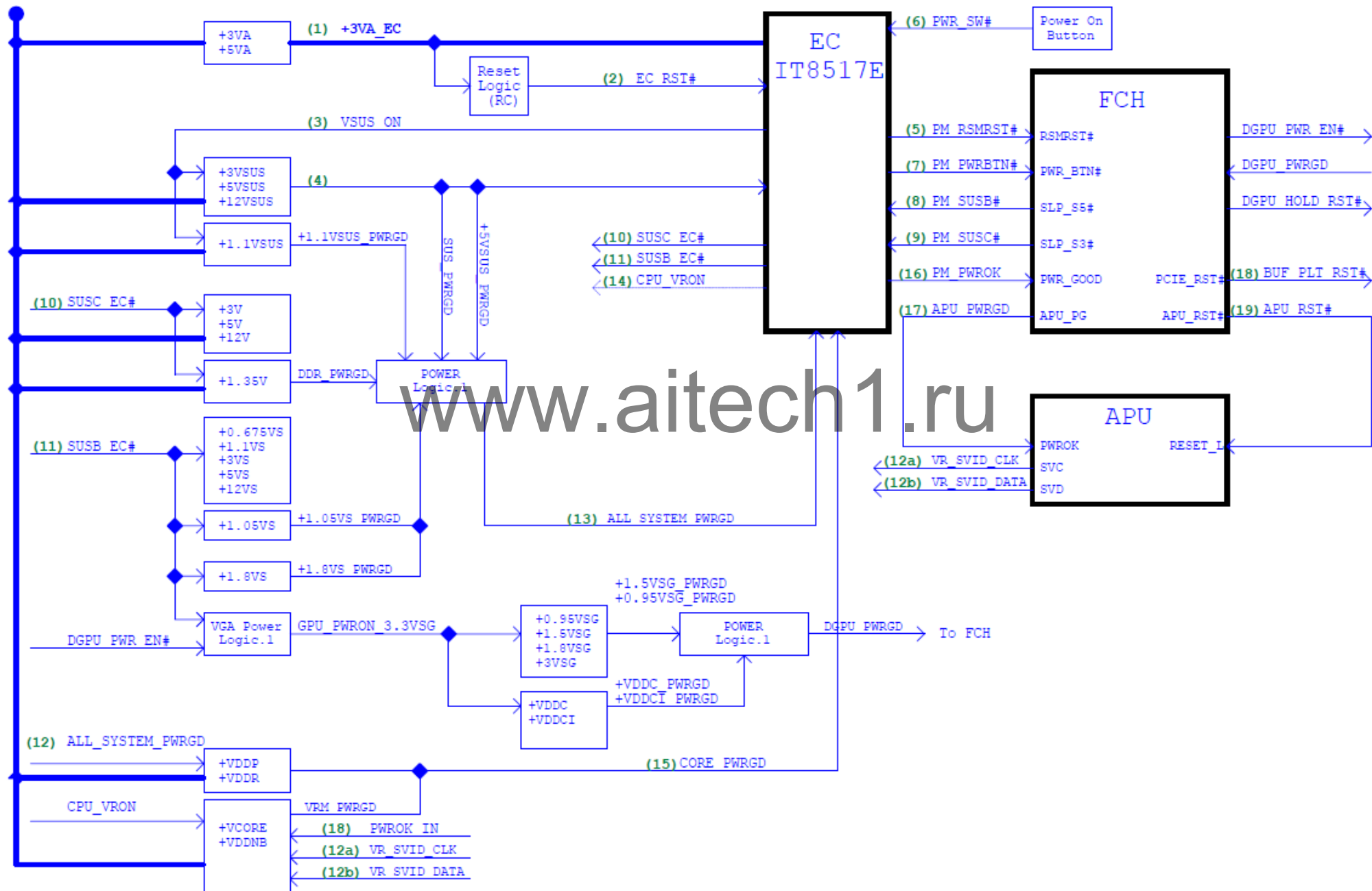


POWER FLOW

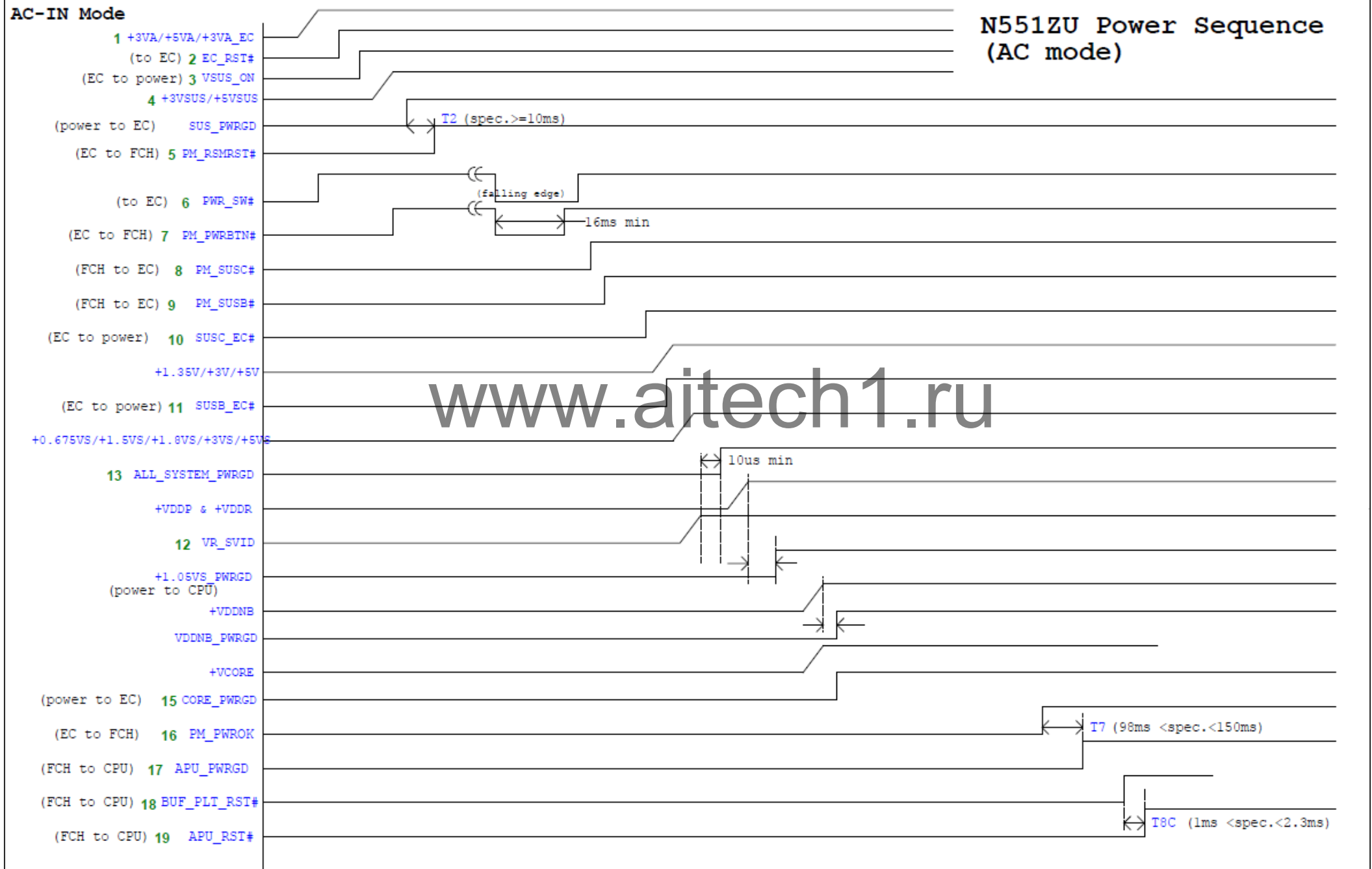


POWER ON SEQUENCE

AC_BAT_SYS



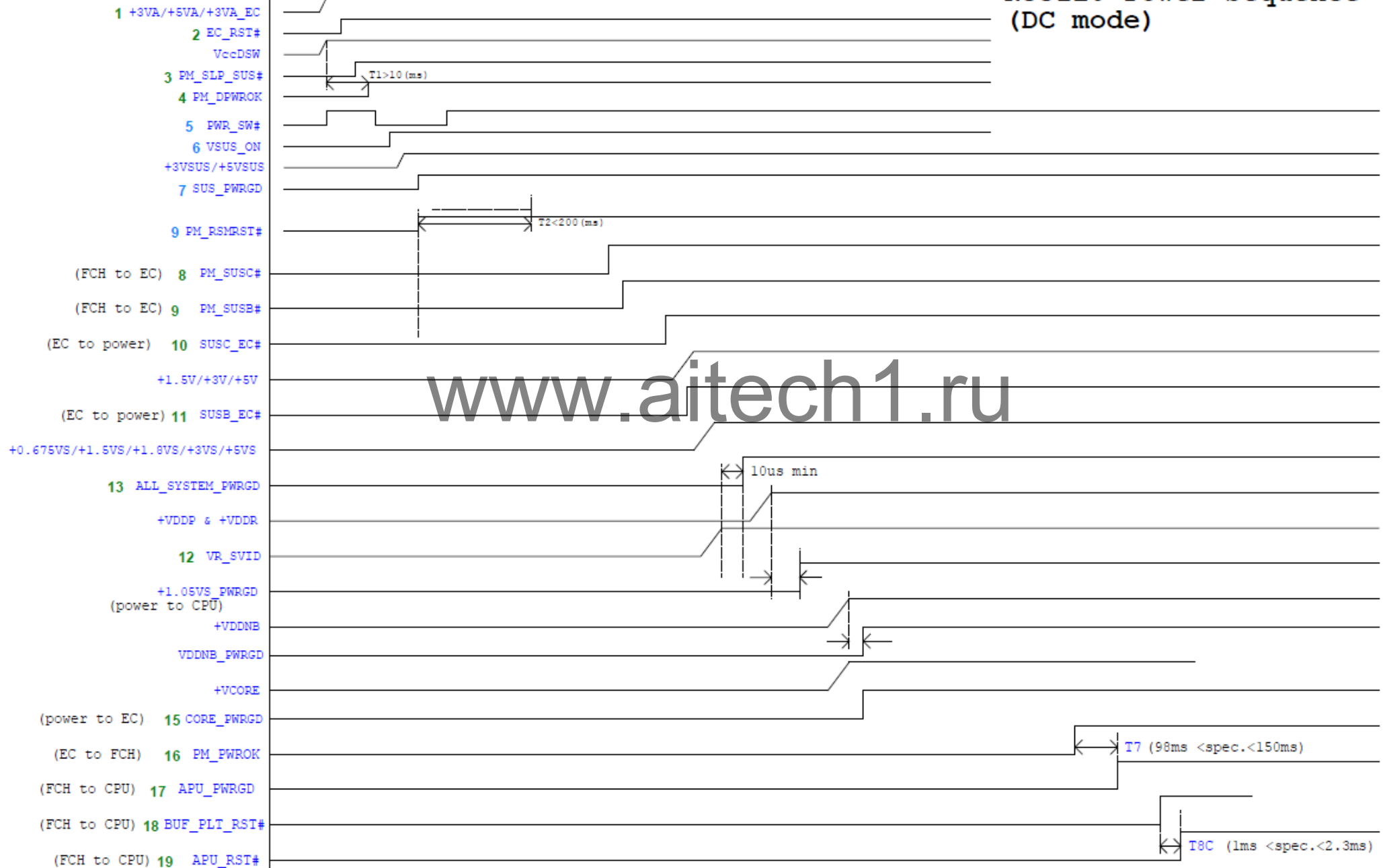
AC_IN POWER ON SEQUENCE



DC_IN POWER ON SEQUENCE

DC-IN Mode

N551ZU Power Sequence
(DC mode)



1 PJP8801.AC_BAT_SYS 454

26 PL8001+VCORE 11

27 PL8004 + VDDNB 12

18 R0510 VR-SVID_CLK 415

29 L8400 +1.8VS 257

19 R0516 VR-SYID_DATA 414

30 PL8200 + VDDP 40

28 PL8301 +1.35V 116

24 Q3201 BUF_PLT_RST# 506

31 PL8601 +1.1VSUS 170

9 VT268 5VSUS PWRGD 682

14 R2122 PM_USB#

13 R2123 PM_SUSC# 622

10.VT842 PM_RSMRST# 686

34 PL8101 +5VSUS 451

2. PSL8100 + 5VAO 504

35 PL8100 +3VSUS 358

8 PSL8104 3VSUS_PWRGD 497

6 PSL8105 3VSUS_ON 573

3.PSL8101 +3VAO 429

7 PSL8103 5VSUS_ON 549

15 PQ9106 SUSC EC# 739

Signal Measure Point-Top

